

UNITED STATES PATENT AND TRADEMARK OFFICE
CERTIFICATE OF CORRECTION

PATENT NO. : 7,140,012 B2
APPLICATION NO. : 09/912571
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INVENTOR(S) : Pugh et al.

Page 1 of 1

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

Column 3

Line 50, "...processor include..." should read --...processor includes...--.

Column 4

Line 42, "...multiple version..." should read --...multiple versions...--.

Column 8

Line 35, "...thus, will not..." should read --...thus, it will not...--.

Line 39, "...library, a predecessor..." should read --...library, or a predecessor...--.

Line 40, "... more generation removed..." should read --...more generations removed...--.

Line 43, "...library, a predecessor..." should read --...library, or a predecessor...--.

Line 45, "...more generation removed..." should read --...more generations removed...--.

Column 9

Line 10, "...successively..." should read --...successively...--.

Line 61, "...thus, will not..." should read --...thus, it will not...--.

Line 65, "...library, a predecessor..." should read --...library, or a predecessor...--.

Line 66, "...more generation removed..." should read --...more generations removed...--.

Column 10

Line 2, "...library, a predecessor..." should read --...library, or a predecessor...--.

Line 4, "...more generation removed..." should read --...more generations removed...--.

Line 9, "... lock 24..." should read --...block 24...--.

Line 27, "...the desire version..." should read --...the desired version...--.

Signed and Sealed this

Twenty-sixth Day of June, 2007



JON W. DUDAS
Director of the United States Patent and Trademark Office